

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: B. Kuratomi, et al.
Application No.: TBD
Filed: October 10, 2003
For: FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE
Group: TBD
Examiner: TBD

PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 10, 2003

Sir:

Please amend the above-identified application, prior to examination thereof,
as listed in the following, and as set forth on the following pages:

Amendments to the Claims;

Remarks are included following the amendments.